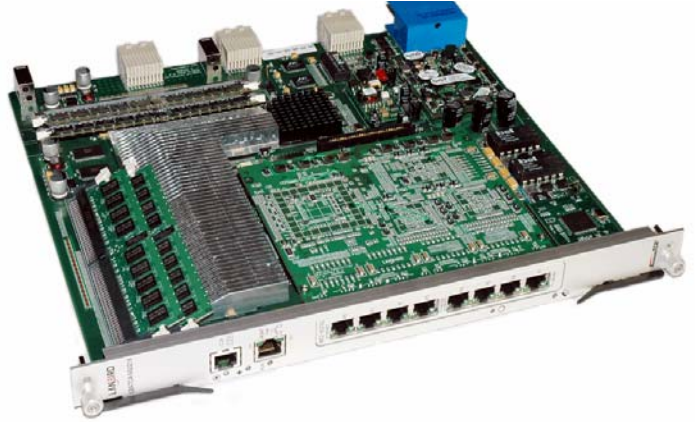


Dual CN5860 NSP
New NS0216A

Advanced TCA®



The LBATCA-NS0216A is a high performance network service processor board to take a key role in next-generation telecommunication equipment market. The LBATCA-NS0216A is compliant with PICMG 3.0 and PICMG 3.1(Option.3).

The LBATCA-NS0216A have two Oceon NSB (Network Service Processor) and each NSP is configured with sixteen MIPS64r2 core for network processing. Each cores are connected with high performance bus and perform to high-performance/multi-function network service blade with all accelerator.

The Oceon Network Service Processor has loaded a network accelerator for high-performance packet processing and be able to perform a role as high performance application linecard with programmability of general-purpose processor.

The Oceon NSP has a variable hardware accelerator built-in. In case of Security Accelerator, it provides a best performance in Ipsec protocol processing through hardware processing with DES/3DES/AES code algorithm and MD5/SHA solving algorithm. The TCP Accelerator providing a concurrent processing for each application flow through the hardware pre-processing in L2-L4 layer. The Regular Expression Accelerator provides the function as IDS/IPS equipment with high-speed pattern matching. The primary unit of accelerator is a packet order unit to execute it as concurrent processing of sixteen cores. Therefore, the Oceon NSP provides the high performance because of all packets are handled to parallel or pipeline method.

The Oceon NSP provides the latest high-speed memory interface. The Oceon NSP supports a 144bit dual-channel registered DDRII-800 SDRAM with ECC. It has a RLDRAM/FCRAM interface for content matching.

The LBATCA-NS0216A provides eight 10/100/1000 Base-T interface in front panel. It also provides eight mini-GBIC(SFP) interface if media interface module is removed. The rear transition module of LBATCA-NS0216A provides two 10/100/1000 Base-T interface.

The LBATCA-NS0216A provides two 10/100/1000 Base-T interface to the PICMG 3.0 Base Interface and eight 10/100/1000 Base-T interface to the PICMG 3.1 Fabric Interface.

The LBATCA-NS0216A provides serial interface and 10Mbps Ethernet interface for management also provides separated serial interface for IPMC management.

Dual Oceon NSP (Network Services Processor)

- NSP: Oceon™ Plus CN5860
- 16 x 1GHz(maximum) MIPS64 R2 cores
- MIPS64 R2 core
 - 32KB Icache, 8KB Dcache
 - MIPS64 Release2 Instruction Set
 - Security Accelerator
 - DES/3DES/AES/
 - MD5/SHA1/CRC etc
- 2MB Shared L2 Cache
- Regular Expression Accelerator
- Compression/Decompression Accelerator
- 2 x 10 x 1Gbps Ethernet Interfaces

Memory

- DDR2 Registered w/t ECC SDRAM
- 144-bit DDR2 up to 800MHz
- Up to 16GB with 4 DIMM

PICMG 3.0 Interfaces

- Two 10/100/1000 BASE-T ports for Base Interface

PICMG 3.1 Interfaces Option 3

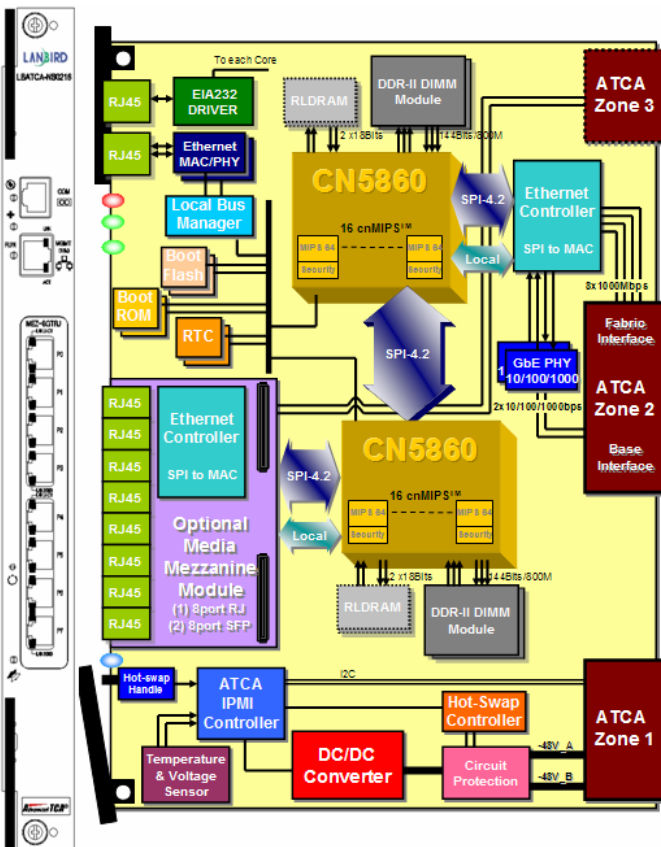
- Eight 1000 BASE-BX ports for Fabric Interface

Front Panel Interfaces

- Media Interface Module
 - Eight 10/100/1000 BASE-T ports
 - Eight Gigabit SFP GBIC ports
- One 10 BASE-TX Management Ethernet port (RJ45)
- One Console port (RJ45)

Rear Panel Interfaces

- Two 10/100/1000 BASE-T ports



LBATCA-NS0216A Diagram

